

CJAC3  
3.2 x 2.5 x 0.9 mm  
Leadless Ceramic Package

### Features

- Quartz crystal controlled PLL Based Square Wave Oscillator
- CMOS Output
- Enable/Disable Function on pad 1 (Pad 2 option)
- 2.5V and 3.3V Supply Voltages

### Applications

Driving A/Ds, D/As, FPGAs  
Fibre Channel  
Ethernet, GbE, SynchE  
Medical  
Storage Area Networking  
COTS  
Telecom  
PON

### Electrical Characteristics

| Parameter                                      | Min   | Typ  | Max                 | Unit   | Condition  |
|--|---|--|---------------------|--------|--|
| Frequency Range                                | 10  |  | 250                 | MHz    |  |
| Frequency Stability                            | ±25   | -  | ±50                 | ppm    | Includes Vcc change, load change, 1 year aging at 25°C ± 2°C, shock, vibration, 25°C tolerance and operating temperature |
| Operating Temperature Range                    | -20<br>-40  | -  | +70<br>+85          | °C     |  |
| Storage Temperature Range                      | -55   |  | +125                | °C     |  |
| Supply Voltage <sup>1</sup> V <sub>CC</sub>    | 2.375<br>2.97   | 2.5<br>3.3                                   | 2.625<br>3.63       | V      | TVcc ramp = 100µs min  |
| Supply Current I <sub>CC</sub>                 | -   | 20   | 40                  | mA     | CL = 15 pF   |
| Disable Current                                | -   | 16   | -                   | mA     | Pin 1 low  |
| Output Waveform                                | CMOS  |  |                     |        | CL = 15 pF   |
| Output High Voltage (V <sub>OH</sub> )         | 0.9*V <sub>CC</sub>                                     | -  | -                   | V      |  |
| Output Low Voltage (V <sub>OL</sub> )          | -   | -  | 0.1*V <sub>CC</sub> | V      |  |
| Output T <sub>RISE</sub> and T <sub>FALL</sub> |   |  | 0.6                 | ns     | Vth is 10% and 90% of Vcc  |
| Startup Time                                   | -   | -  | 10                  | ms     | Time for output to reach specified frequency   |
| Duty Cycle                                     | 45  | -  | 55                  | %      | At 50% Vcc level   |
| Enable/Disable Pullup                          | -   | 900  | -                   | kΩ     | Pin 1 to Vcc   |
| V <sub>DISABLE</sub>                           | -   | -  | 0.3*V <sub>CC</sub> | V      | Referenced to Ground   |
| V <sub>ENABLE</sub>                            | 0.7*V <sub>CC</sub>                                     | -  | -                   |        |  |
| Phase Noise                                    | 1 kHz<br>10 kHz<br>100 kHz<br>1 MHz<br>10 MHz<br>20 MHz | -111<br>-118<br>-119<br>-134<br>-155<br>-156 | -                   | dBc/Hz | 25°C ± 2°C, 3.3V, 155.52MHz  |
| Phase Jitter                                   | -   | 0.9  | -                   | ps rms | 12 kHz to 20 MHz at 155.52 MHz   |

### Part Number

### Example: CJAC3LZ-A7BP-100.0TS

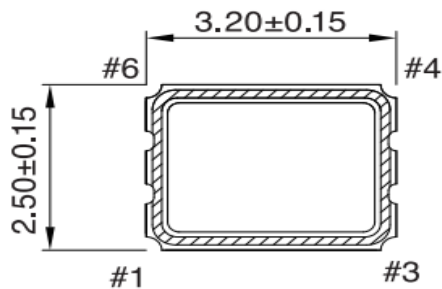
| Series Model | Output   | Package Size  | Supply Voltage       | Packaging                          | Operating Temperature Range            | Frequency Stability        | Frequency (MHz) | Output Control |
|--------------|----------|---------------|----------------------|------------------------------------|--|----------------------------|-----------------|----------------|
| CJA          | C        | 3             | L                    | Z                                  | A7                                     | BP                         | 100.0           | TS             |
|              | C = CMOS | 3 = 3.2x2.5mm | S = 2.5V<br>L = 3.3V | Blank = Tape only<br>Z = Tape/Reel | A5 = -20 to +70°C<br>A7 = -40 to +85°C | BR = ±25ppm<br>BP = ±50ppm |                 | TS=TRISTATE    |

Notes: Specifications with Pad 1 E/D open circuit

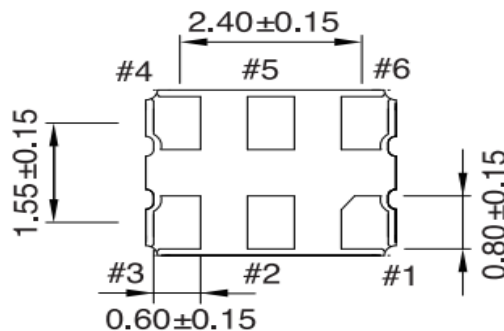
<sup>1</sup> Place an appropriate power supply bypass capacitor next to device for correct operation

### Mechanical Dimensions (mm)

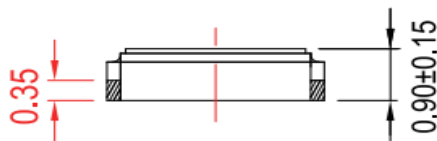
[ TOP VIEW ]



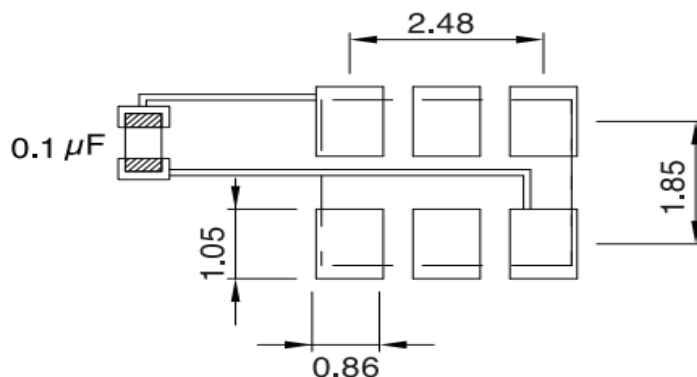
[ BOTTOM VIEW ]



[ SIDE VIEW ]



| Pin# | Function       |
|------|----------------|
| 1    | Tri-State      |
| 2    | NC             |
| 3    | GND            |
| 4    | Output         |
| 5    | Comp. Output   |
| 6    | Supply Voltage |



| Enable/Disable |           |
|----------------|-----------|
| Pin 1          | Output    |
| Open           | Active    |
| Logic '1'      | Active    |
| Ground         | Tri-state |

Pad Layout mm shown

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

To ensure optimal oscillator performance, place a by-pass capacitor of 0.1 µF as close to the part as possible between Vdd and GND pads.

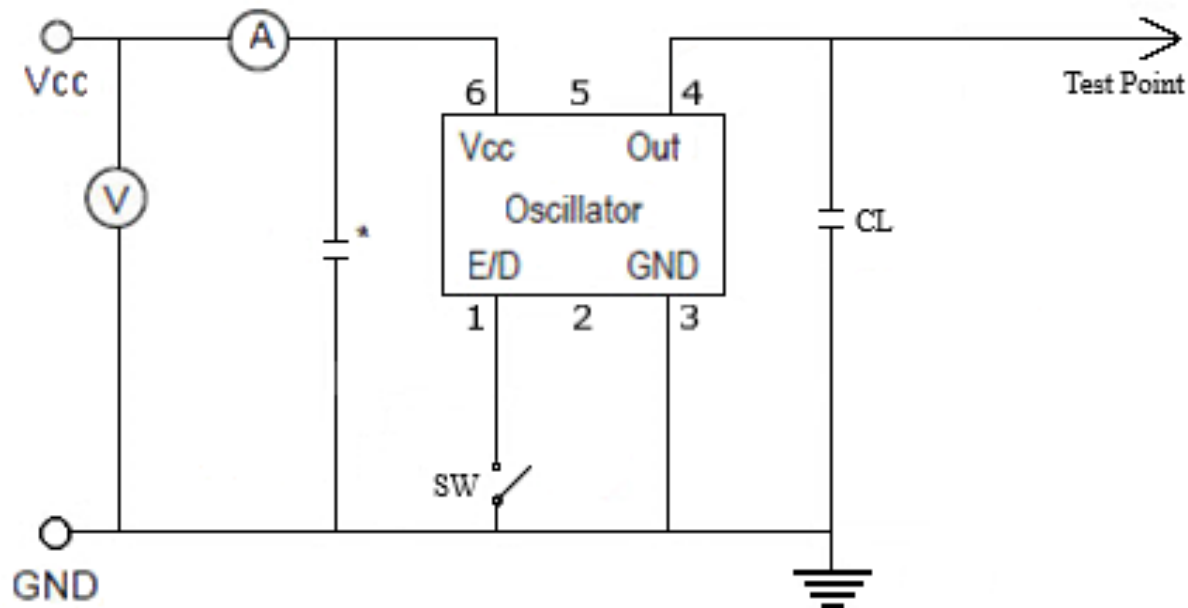
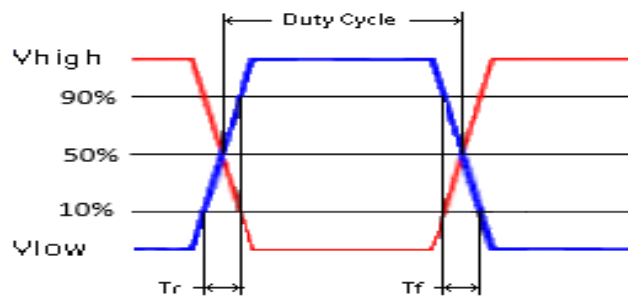
Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

Cardinal Components certifies this device is in accordance with the RoHS and REACH directives.

Cardinal Components guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's  
Weight of the Device: 0.028 grams  
Moisture Sensitivity Level: 1 As defined in J-STD-020D

For Optimum Jitter Performance, Cardinal recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans

**Electrical Test /Load Circuit**

**Test Waveform**

**Environmental / ESD**
**Reliability: Environmental Test**

| Parameter        | Reference Standard           | Test Condition  |
|------------------|------------------------------|---|
| Vibration        | MIL-STD-883 2007 Condition A | 10-2000Hz, 1.52mm, 20g, each axis for 4hrs                |
| Thermal Shock    | MIL-STD-883 1010 Condition B | -55°C, 125°C, soak time is 10 mins, with total 200 cycles |
| Mechanical Shock | MIL-STD-883 2002 Condition B | 1500g, half-sine, 0.5ms, each axis for 3 times            |

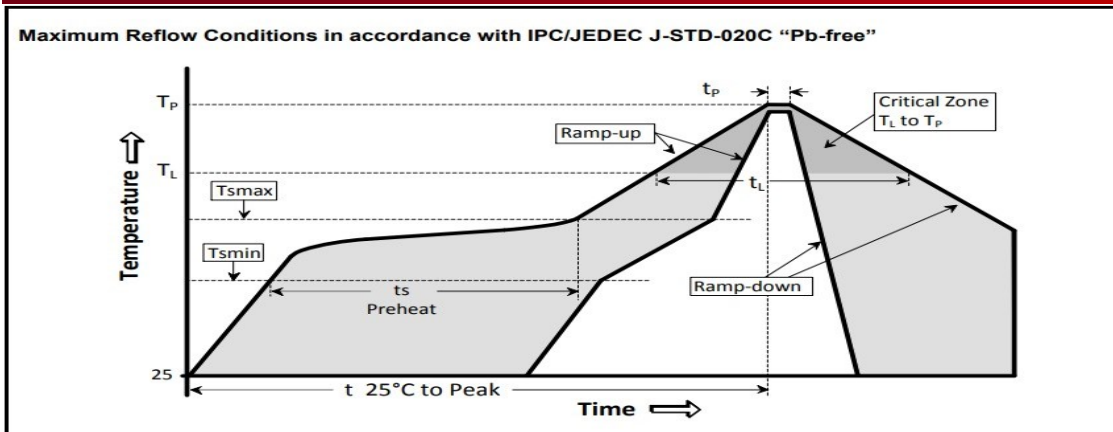
**Absolute Maximum Ratings**

| Parameter                      | Unit                            |
|--------------------------------|---------------------------------|
| V <sub>CC</sub> Supply Voltage | -0.5V to +4.2V                  |
| V <sub>i</sub> Input Voltage   | -0.5V to V <sub>CC</sub> + 0.5V |
| V <sub>o</sub> Output Voltage  | -0.5V to V <sub>CC</sub> + 0.5V |
| Max Junction Temperature       | 125°C                           |

**ESD Rating**

| Model                | Min. Voltage | Condition   |
|----------------------|--------------|-------------|
| Human Body Model     | 2000V        | JESD22-A114 |
| Charged Device Model | 1000V        | JESD22-C101 |
| Machine Model        | 120V         | JESD22-A115 |

## Reflow Cycle

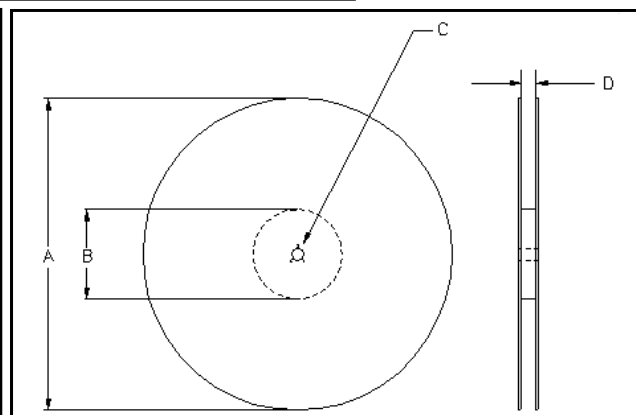
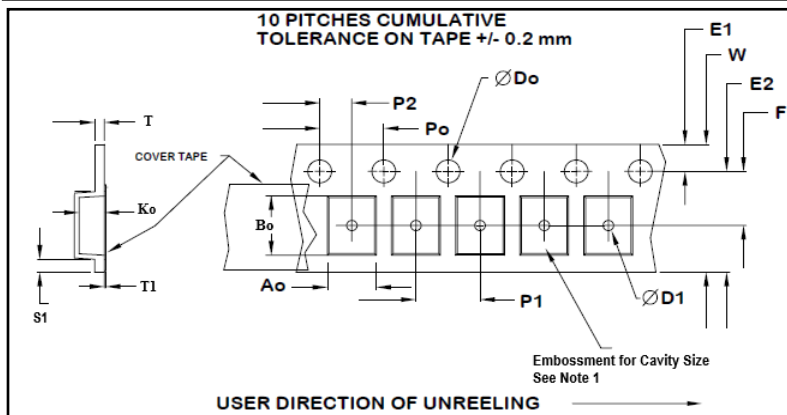


The part may be reflowed 2 times without degradation (typical for lead free processing).

| Temperature Profile                         | Symbol                                 | Condition        | Unit   |
|---|--|------------------|--------|
| Average ramp-up rate                        | (T <sub>Smax</sub> to T <sub>P</sub> ) | 3°C / second max | °C / s |
| Ramp down Rate                              | T <sub>cool</sub>                      | 6°C / second max | °C / s |
| Time 25°C to Peak Temperature               | T <sub>to-peak</sub>                   | 8 minutes max    | min    |
| <b>Preheat</b>                              |  |                  |        |
| Temperature min                             | T <sub>Smin</sub>                      | 150              | °C     |
| Temperature max                             | T <sub>Smax</sub>                      | 200              | °C     |
| Time T <sub>Smin</sub> to T <sub>Smax</sub> | t <sub>s</sub>                         | 60 – 180         | sec    |
| <b>Soldering above liquidus</b>             |  |                  |        |
| Temperature liquidus                        | T <sub>L</sub>                         | 217              | °C     |
| Time above liquidus                         | t <sub>L</sub>                         | 60 – 150         | sec    |
| <b>Peak temperature</b>                     |  |                  |        |
| Peak Temperature                            | T <sub>P</sub>                         | 260              | °C     |
| Time within 5°C of peak temperature         | t <sub>p</sub>                         | 20 – 40          | sec    |

## Tape and Reel

Tape and Reel available for quantities of 250 to 3000 per reel, cut tape for < 250. 8mm tape, 4mm pitch.



Tape Variable Dimensions Table 2

| Tape Size | E2 typ | F            | P1          | W max | Ao      | Bo      | Ko      |
|-----------|--------|--------------|-------------|-------|---------|---------|---------|
| 8mm       | 6.25   | 3.5<br>±0.05 | 4.0<br>±0.1 | 8.2   | 2.7±0.1 | 3.4±0.1 | 1.4±0.1 |

Dimensions in mm      Drawing Not to scale  
Note 1: Embossed cavity to conform to EIA-481-B

Tape Constant Dimensions Table 1

| Type Size | Do                  | D1 typ | E1           | Po          | P2          | S1 min | T max | T1 max |
|-----------|---------------------|--------|--------------|-------------|-------------|--------|-------|--------|
| 8mm       | 1.5<br>+0.1<br>-0.0 | 1.5    | 1.75<br>±0.1 | 4.0<br>±0.1 | 2.0<br>±0.1 | 0.6    | 0.3   | 0.1    |

Reel Dimensions (may vary) Table 3

|           | A      |       | B      |       | C                        | D                                     |
|-----------|--------|-------|--------|-------|--------------------------|---------------------------------------|
| Reel Size | Inches | mm    | Inches | mm    | mm                       | mm                                    |
| 7         | 7.0    | 177.8 | 2.50   | 63.5  | 13.0<br><br>+0.5<br>-0.2 | Tape size<br>+0.4<br><br>+2.0<br>-0.0 |
| 10        | 10.0   | 254.0 | 4.00   | 101.6 |                          |                                       |
| 13        | 13.0   | 330.2 | 3.75   | 95.3  |                          |                                       |

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